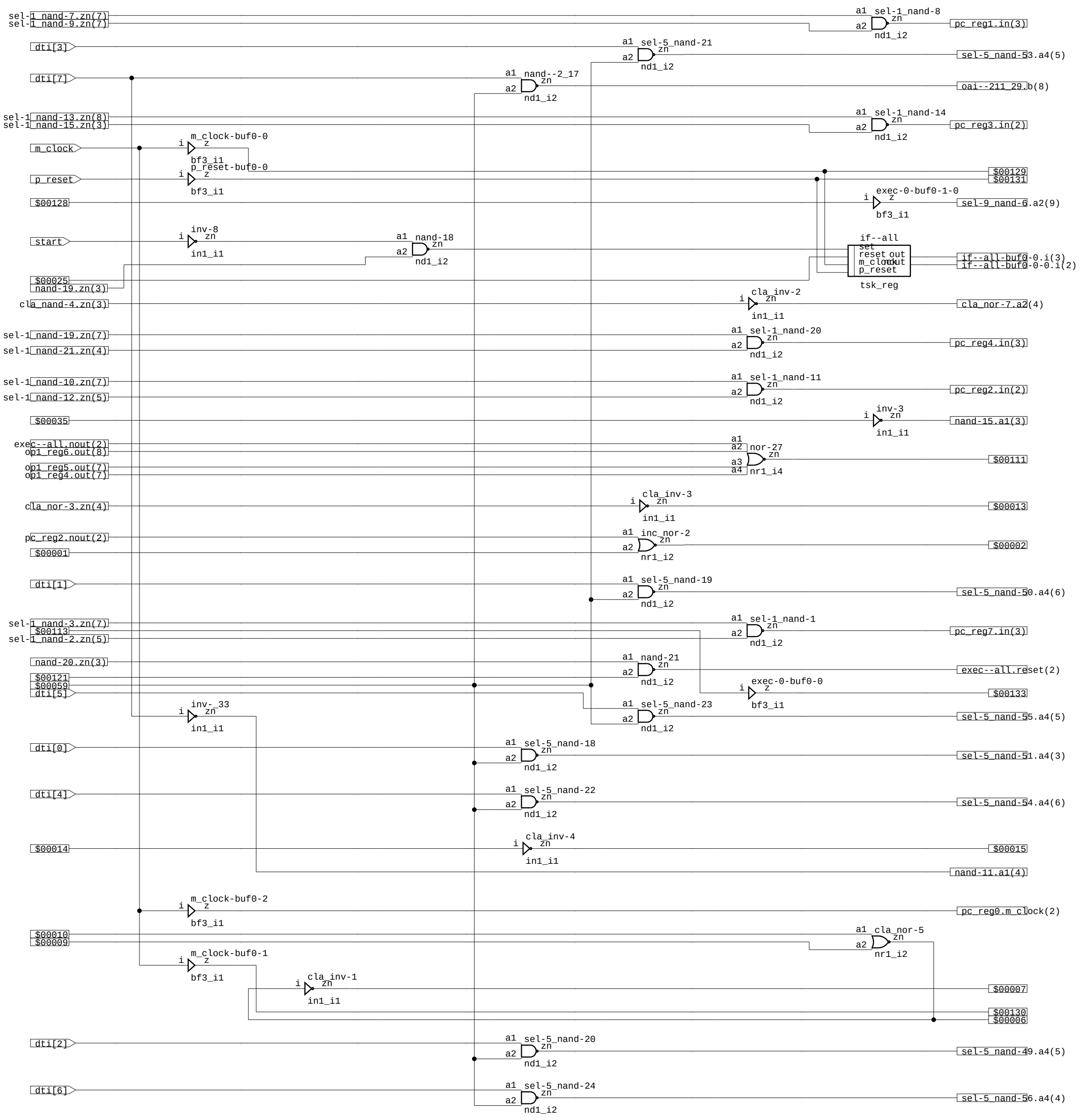

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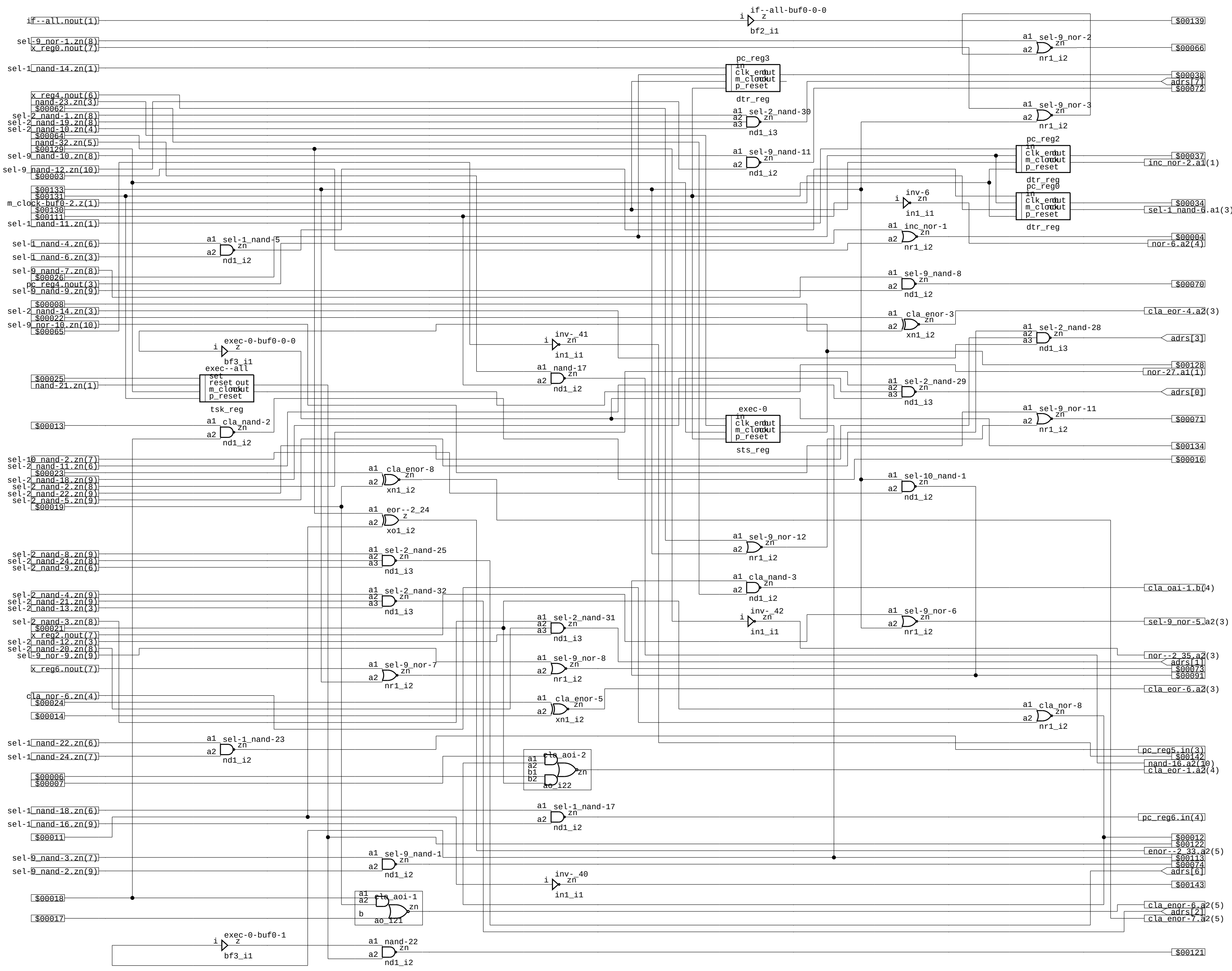
Date : 95/07/13 16:40:24	Sheet : 1 of 11	Rev : 2.3



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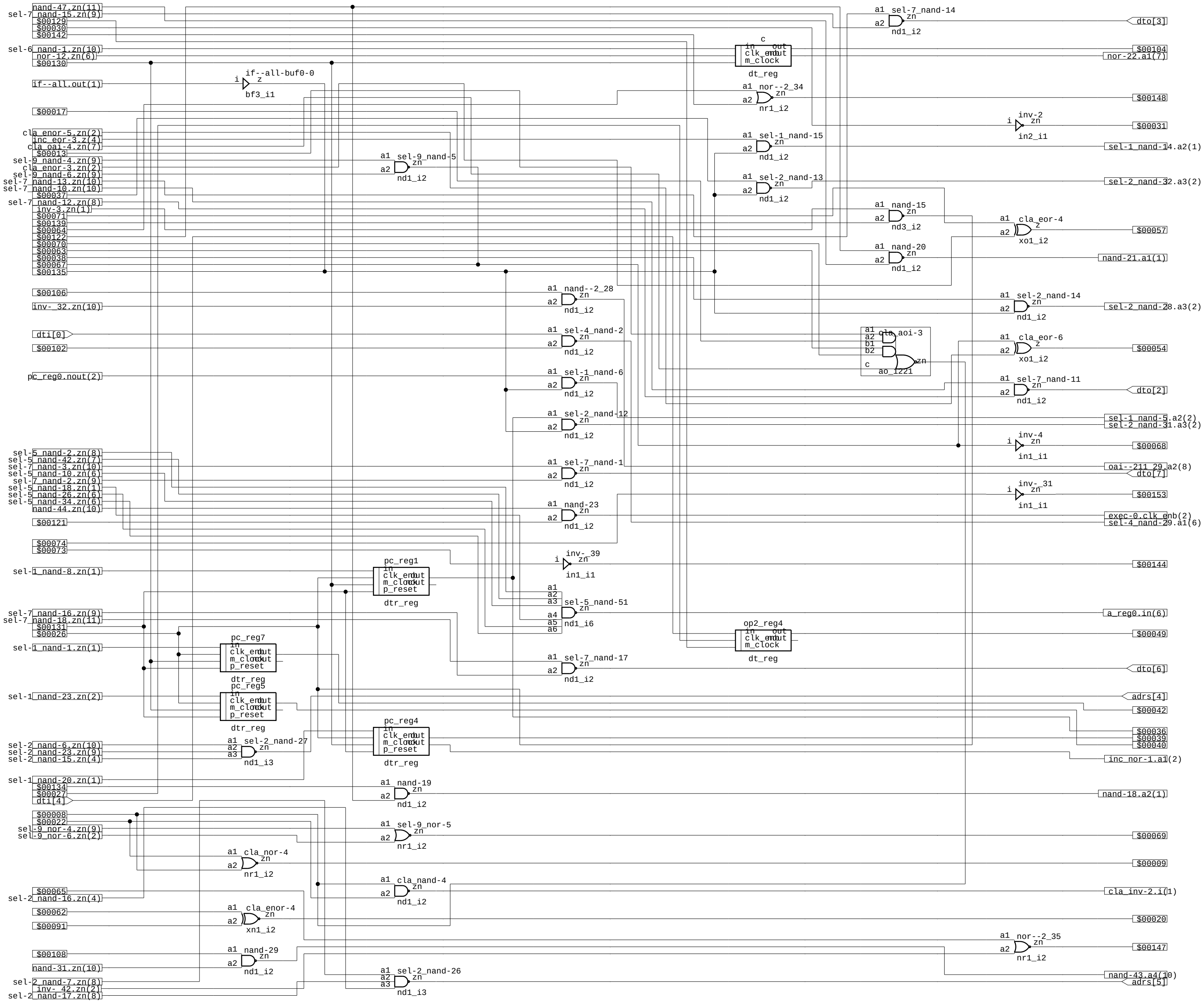

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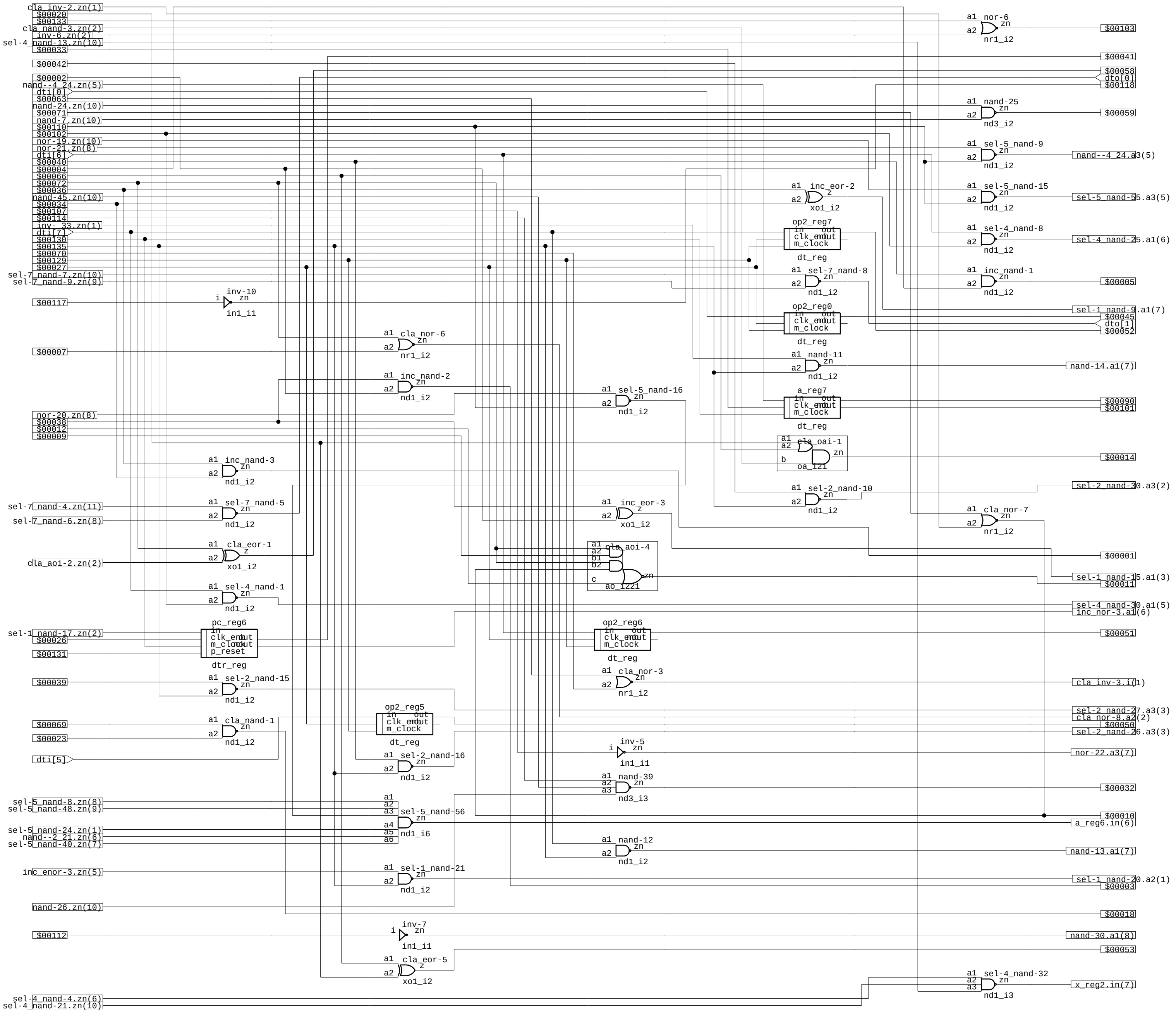
Date : 95/07/13 16:40:26	Sheet : 3 of 11	Rev : 2.3



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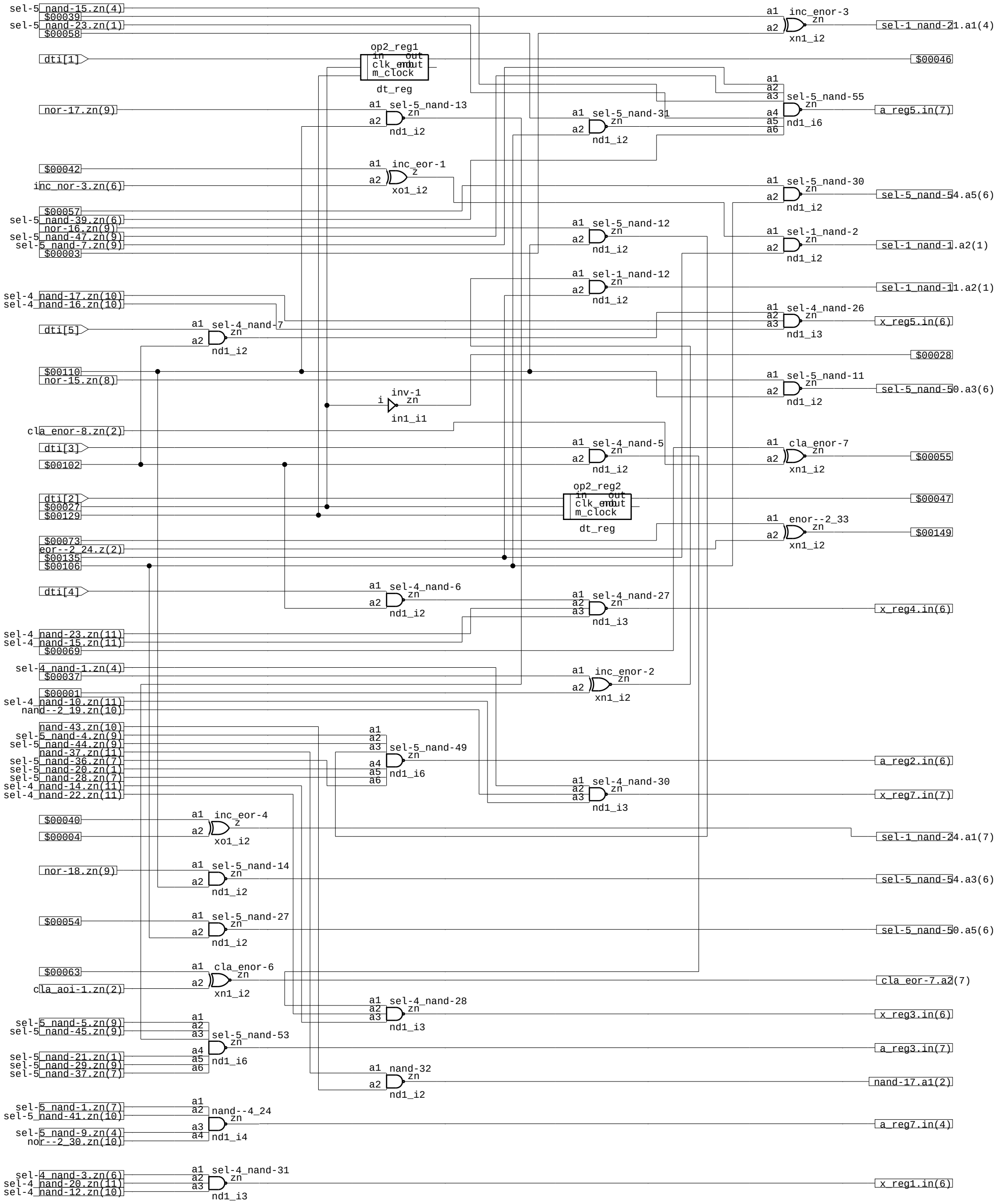
Title : cpu		
Date : 95/07/13 16:40:28	Sheet : 4 of 11	Rev : 2.3



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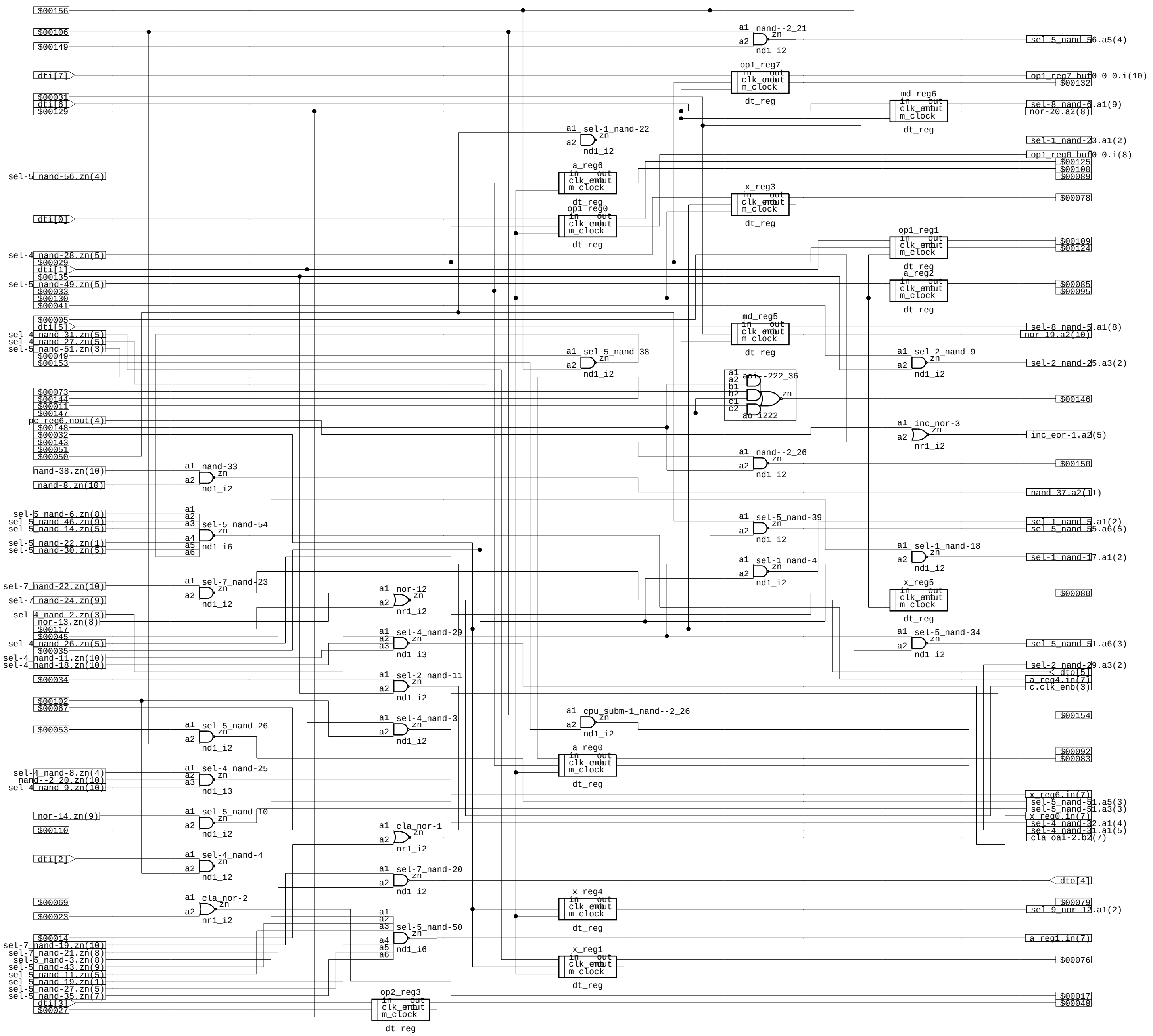
Date : 95/07/13 16:40:29	Sheet : 5 of 11	Rev : 2.3



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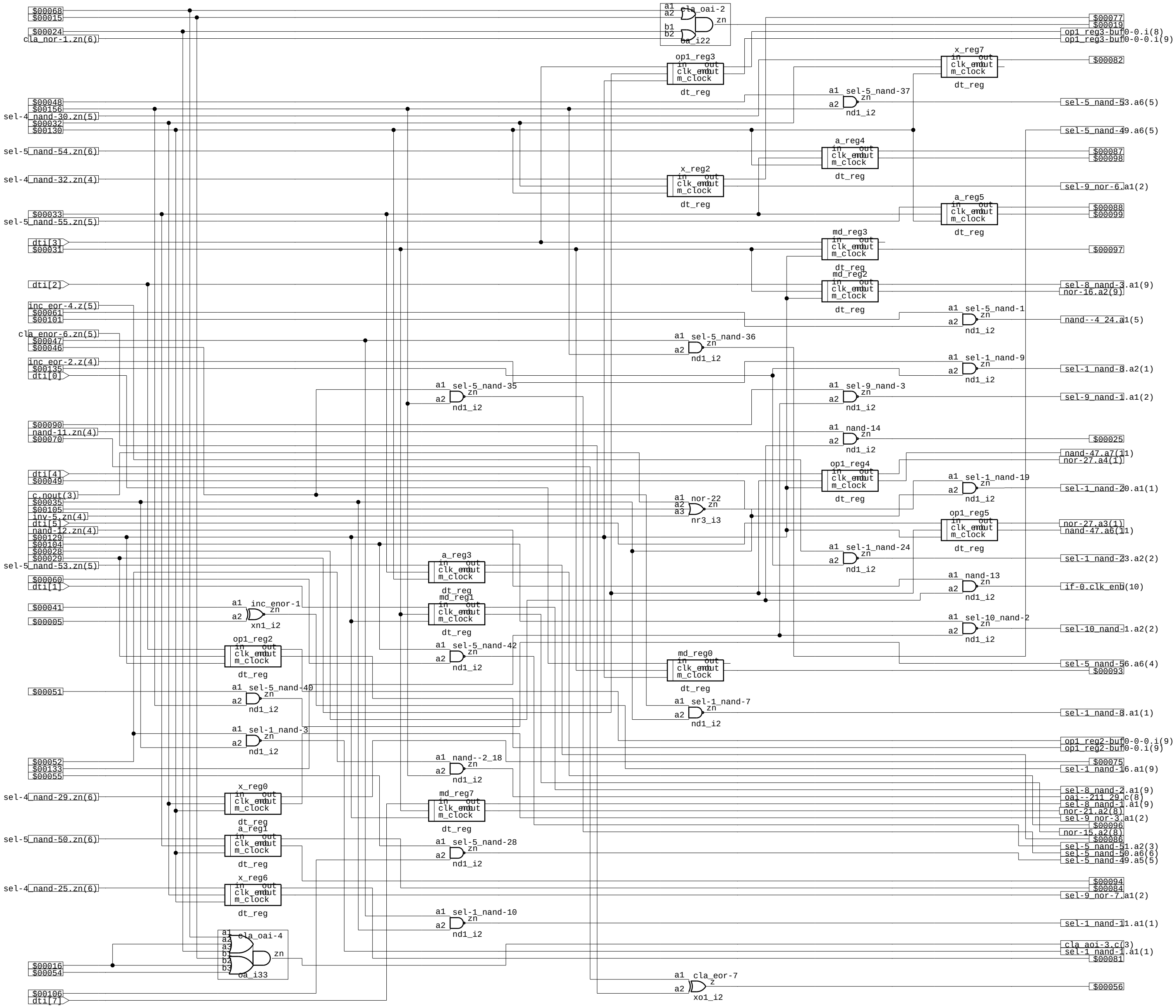
Date : 95/07/13 16:40:30	Sheet : 6 of 11	Rev : 2.3



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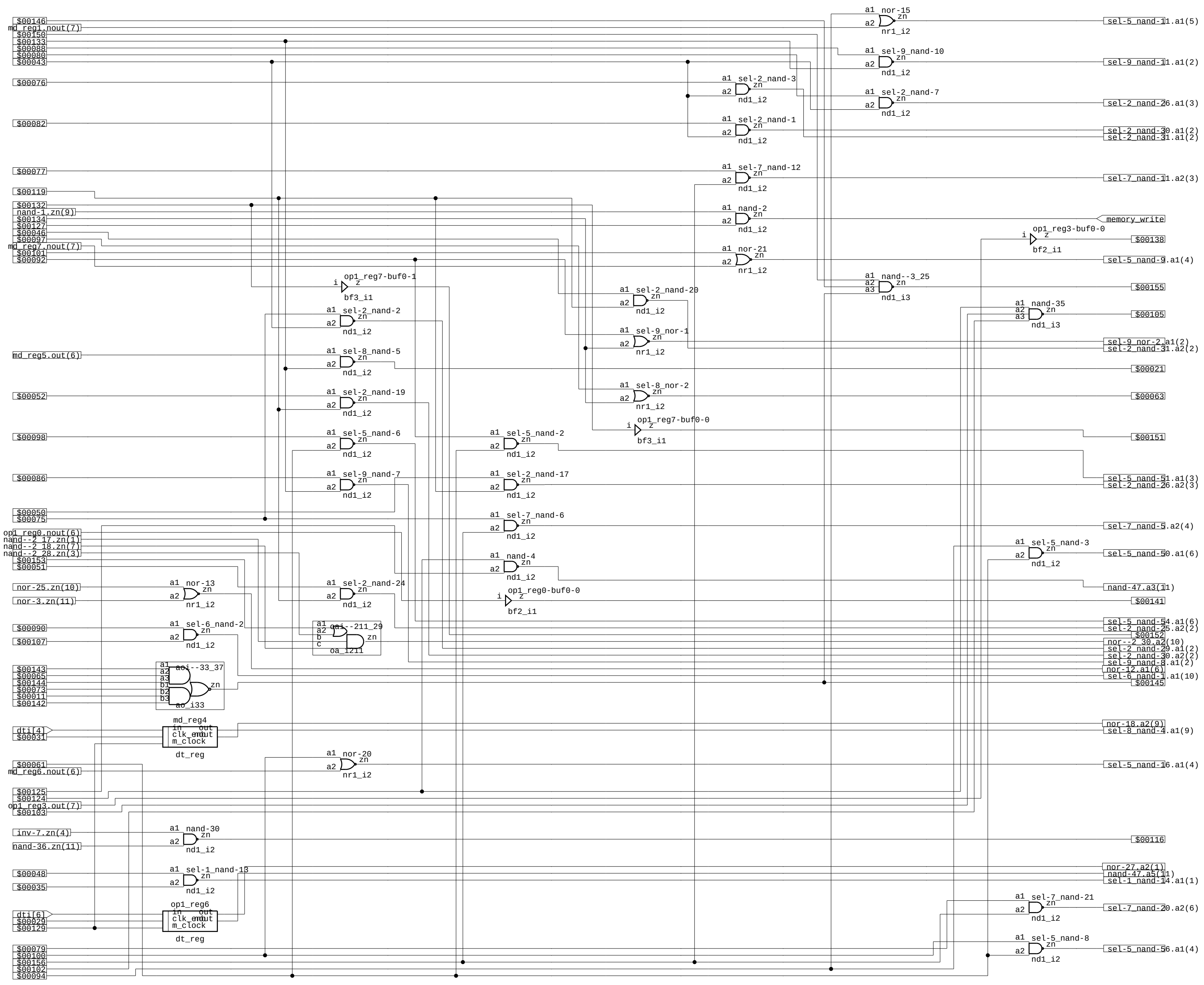
Date : 95/07/13 16:40:31	Sheet : 7 of 11	Rev : 2.3



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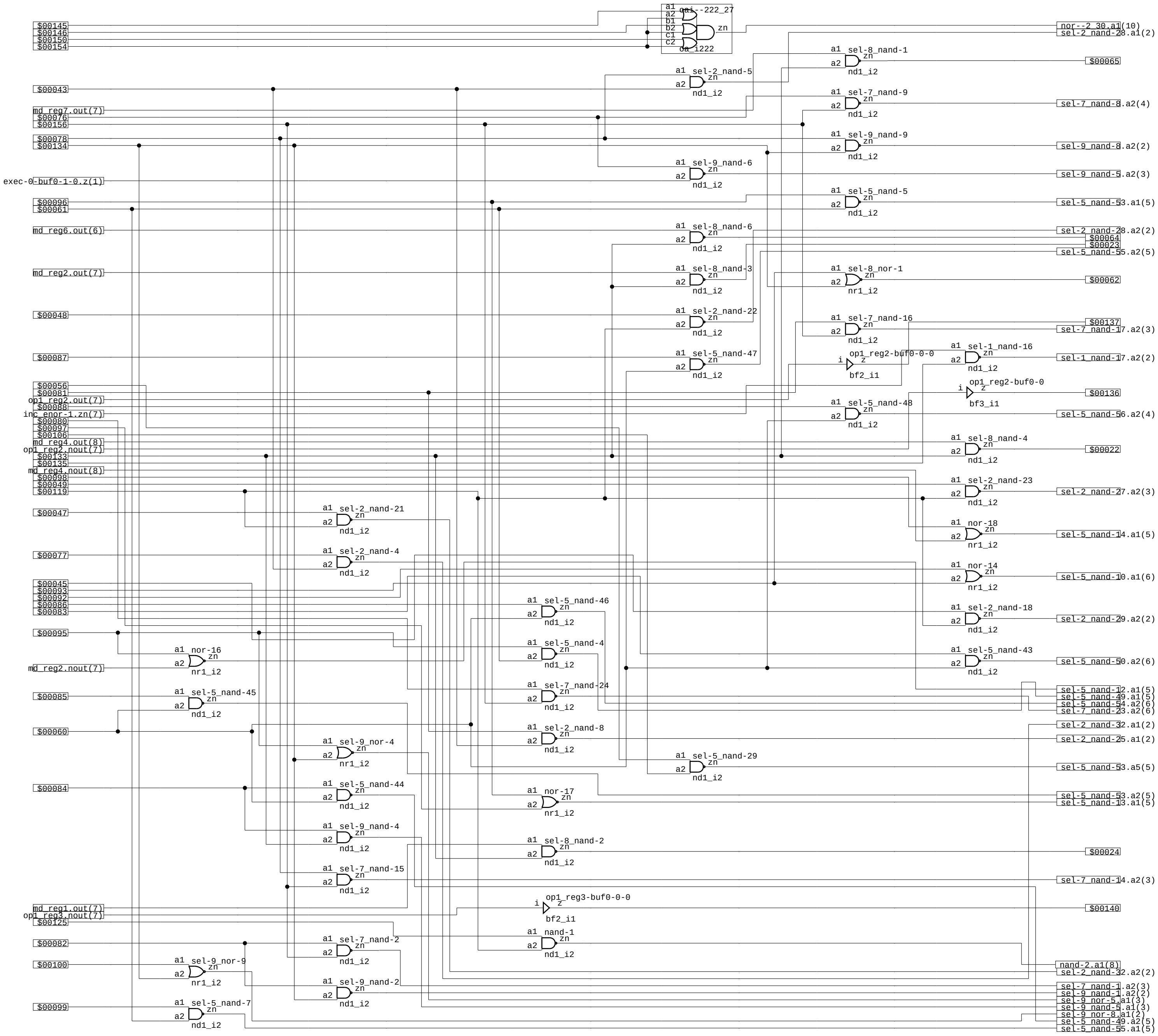
Date : 95/07/13 16:40:33 | Sheet : 8 of 11 | Rev : 2.3



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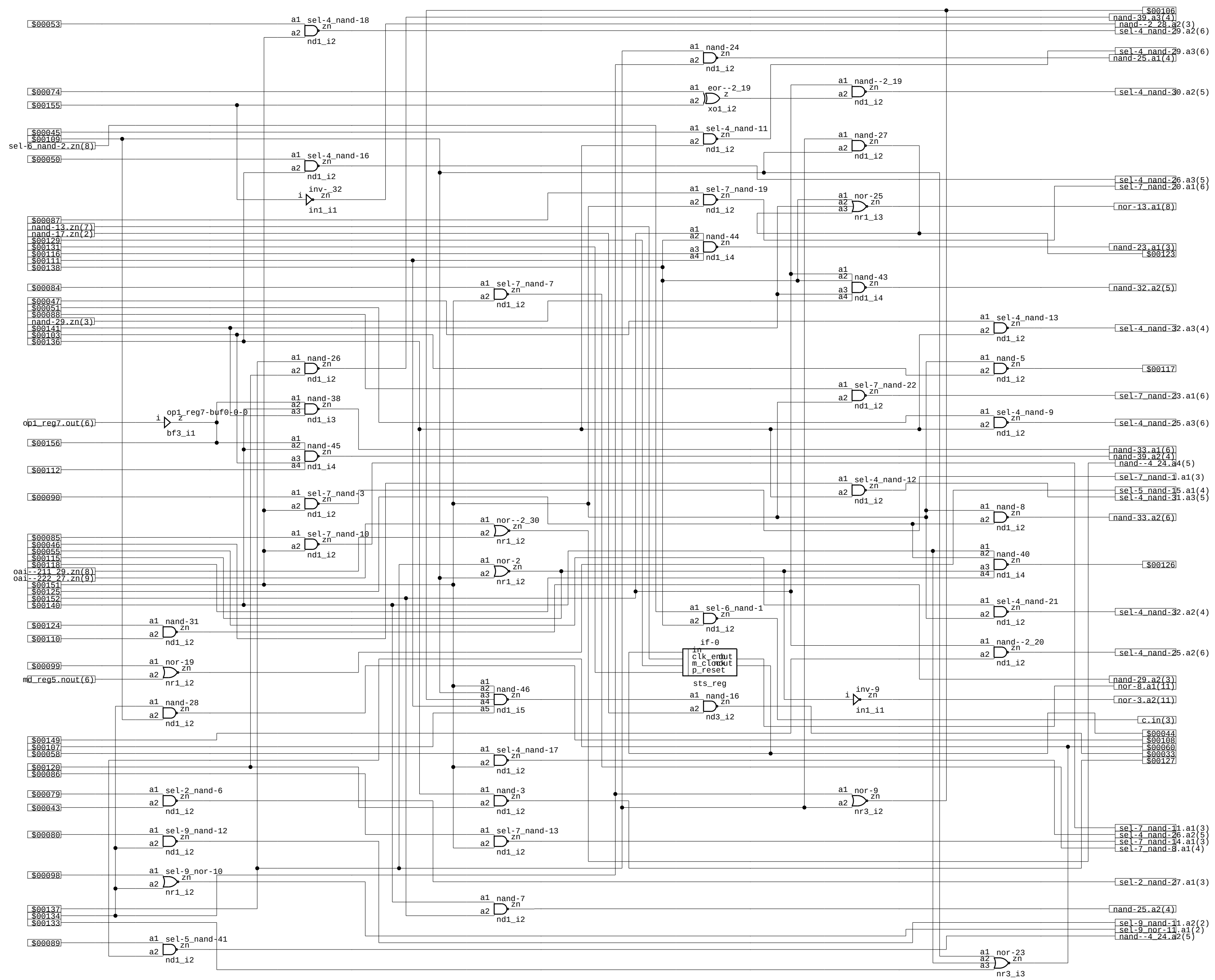
Date : 95/07/13 16:40:34	Sheet : 9 of 11	Rev : 2.3



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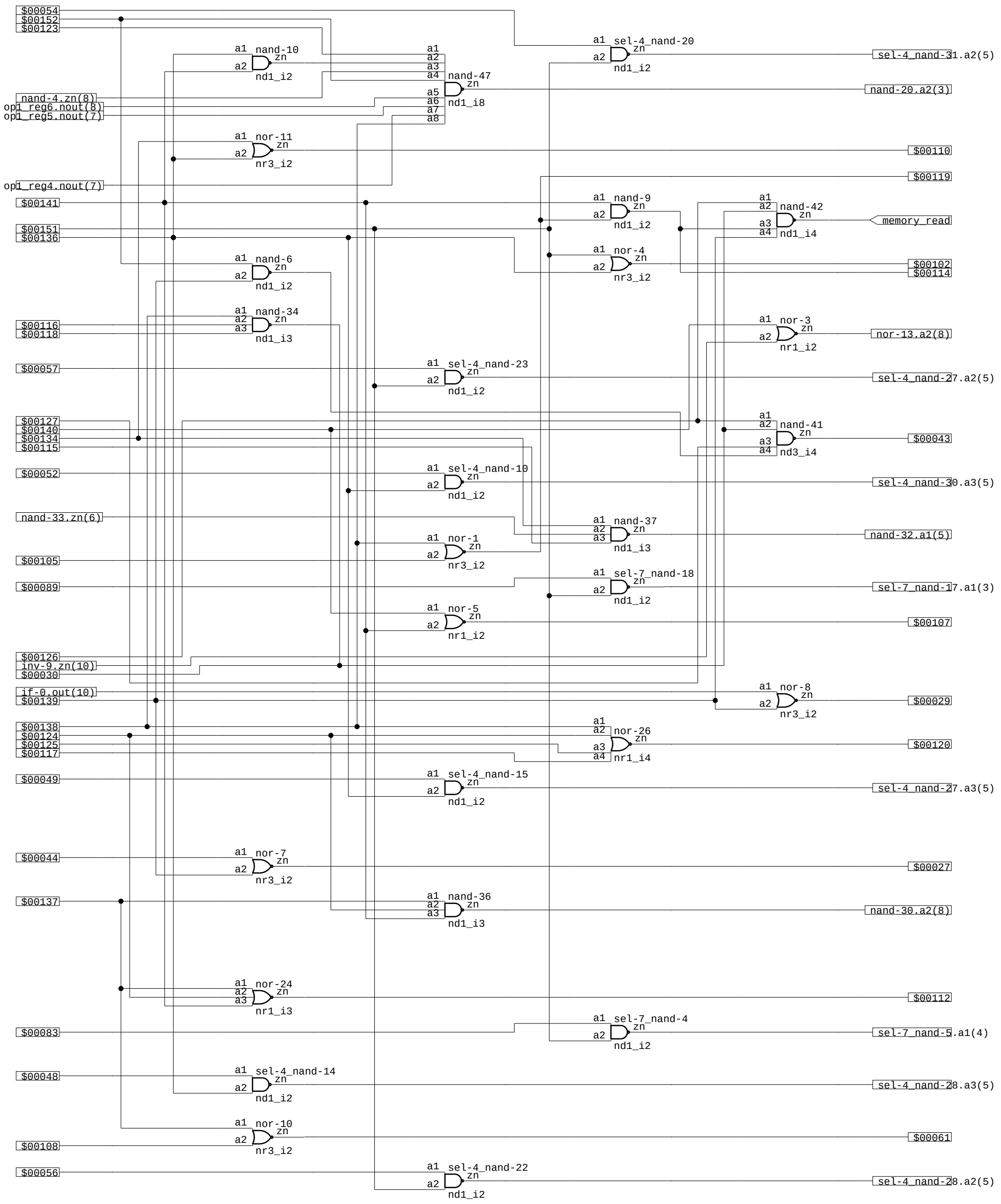
Date : 95/07/13 16:40:35	Sheet : 10 of 11	Rev : 2.3



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Title : cpu

Date : 95/07/13 16:40:37 | Sheet : 11 of 11 | Rev : 2.3